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| G:\Photo\NSU campus\Logo\nsu_big (BW).jpg | North South University  Department of Electrical & Computer Engineering |

**EEE 141 Lab | Electrical Circuits I Lab**

*Fall 2018*

**Course Faculty:** Zunayeed Bin Zahir

**Lab Instructor:** Mirza Sarwar Kamal  
**Email:**  sarwar.kamal@northsouth.edu  
**Mobile:** 0191 338 58 13  
**Office Room:** TBA  
**Office Hours:** T 11:30 AM – 5:00 PM

W 11:30 PM – 2:30 PM

**Lab Section:** 12   
**Lab Timing:** T 8:00 AM – 11:00 AM   
**Lab Room:** SAC 504

**FB Group:** EEE141/Lab/DC/ins:Kamal

**Lab Outline**

1. **Orientation of lab instruments & Verification of Ohm’s law.**
2. **Series and Parallel networks.**
3. **Ladder Circuit and Loading Effect of Voltage Divider Circuit.**
4. **Superposition Theorem.**
5. **Delta-Wye conversion techniques.**
6. **Thevenin and maximum power transfer theorem.**
7. **RC network using PSpice.**

**Marks Distribution (Tentative)**

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| Attendance and Performance: | 10 % |
| Lab Report: | 10 % |
| Quiz | 10% |
| Assignment/Homework | 15% |
| Viva + Practical setup Final | 25 % |
| Written Final Exam: | 30 % |